



LibreSilicon.com

F-Si.org

CERN OHL

Home CMOS

Free Silicon Conference FSiC

▷ CPU

RISC V

▷ RAM

<https://OpenRAM.soe.ucsc.edu>

▷ Analog

<https://LibreSilicon.com> 555 "soon"

▷ Power

?

5  $\mu\text{m}$   $\approx$  2  $\mu\text{m}$

~~< \$1,000~~

\$995

Black Box Labs

UCSD MEMS Lab

Simon Fraser U.

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UV laser direct write system

# Libre Silicon.com

HKUST NFF clean room

¿ How can ~~you~~ <sup>WE</sup> HELP? (2019 March 24 Sunday)

- ① Funding
- ② Community managers
- ③ project management
- ④ <https://github.com/foshardware/LSC>  
needs
  - Timing-oriented synthesis
  - SAT based placement
- ⑤ Access to academic foundries
- ⑥ Lawyerized licenses/contracts